# **DRAGNET Cluster Benchmark Numbers**

Parts of the cluster and interconnects have been stress tested to optimize configuration and to find upper performance bounds that can be useful for application optimization and rough capacity estimates.

The tests described tend to cover \*maximum\* achievable performance results on a synthetic ideal workload. It is very likely that your (real) application will never reach these numbers, as the workload is non-ideal, and reaching peak performance can take a lot of effort. But these numbers can serve as a top reference.

**Cluster Specifications** 

# **CPU computing and memory**

Computing and memory numbers N/A

# **GPU computing, memory, PCIe**

Computing numbers N/A

## Memory and PCIe Bandwidth

For PCIe bandwidth, there is a substantial difference between the 2 local GPUs and the 2 GPUs local to the other CPU in the same node. NVIDIA's bandwidthTest is not meant for performance (because of GPU Boost) (as it says), but here are the numbers anyway:

```
[amesfoort@drg23 bandwidthTest]$ ./bandwidthTest --device=0
[CUDA Bandwidth Test] - Starting...
Running on...
Device 0: GeForce GTX TITAN X
Quick Mode
Host to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
Transfer Size (Bytes) Bandwidth(MB/s)
33554432 6325.2
Device to Host Bandwidth, 1 Device(s)
PINNED Memory Transfers
Transfer Size (Bytes) Bandwidth(MB/s)
```

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```
33554432
                    4279.2
Device to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    250201.6
Result = PASS
NOTE: The CUDA Samples are not meant for performance measurements. Results
may vary when GPU Boost is enabled.
[amesfoort@drg23 bandwidthTest]$ ./bandwidthTest --device=1
[CUDA Bandwidth Test] - Starting...
Running on...
Device 1: GeForce GTX TITAN X
Quick Mode
Host to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    6178.8
Device to Host Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    4412.2
Device to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    249804.8
Result = PASS
NOTE: The CUDA Samples are not meant for performance measurements. Results
may vary when GPU Boost is enabled.
[amesfoort@drg23 bandwidthTest]$ ./bandwidthTest --device=2
[CUDA Bandwidth Test] - Starting...
Running on...
Device 2: GeForce GTX TITAN X
Ouick Mode
Host to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
  Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    10547.1
```

```
Device to Host Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    10892.2
Device to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    249503.5
Result = PASS
NOTE: The CUDA Samples are not meant for performance measurements. Results
may vary when GPU Boost is enabled.
[amesfoort@drg23 bandwidthTest]$ ./bandwidthTest --device=3
[CUDA Bandwidth Test] - Starting...
Running on...
Device 3: GeForce GTX TITAN X
Ouick Mode
Host to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    10626.5
Device to Host Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    10903.4
Device to Device Bandwidth, 1 Device(s)
PINNED Memory Transfers
   Transfer Size (Bytes)
                            Bandwidth(MB/s)
   33554432
                    249342.6
Result = PASS
```

NOTE: The CUDA Samples are not meant for performance measurements. Results may vary when GPU Boost is enabled.

## Networking

We have benchmarked the infiniband and 10G networks. A good guide is available at the <a href="http://fasterdata.es.net/">http://fasterdata.es.net/</a> under Host Tuning (and to a lesser extend under Network Tuning). But the indicated Linux kernel sysctl knobs did not help; CentOS 7 already has decent settings, and our transfers are all on a low latency LAN (as opposed to wide-area).

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## Infiniband

Each drgXX node has an FDR (54.545 Gbit/s) HCA (Host Channel Adapter) local to the second CPU (i.e. CPU id 1). The 36 port cluster switch is connected to the COBALT switch with 5 aggregated lines (272.727 Gbit/s). See below what can be achieved under ideal circumstances.

#### **IPoIB: TCP and UDP**

An application that uses the Infiniband (ib) network normally uses IPoIB (IP-over-Infiniband) to transfer data via TCP or UDP. DRAGNET IPoIB settings have been optimized for TCP (vs UDP performance) (IPoIB connected-mode enabled). We (mostly) use TCP and will not receive UDP data from LOFAR stations directly.

We used the iperf3 benchmark and got the following bandwidth numbers between two drgXX nodes:

Out-of-the-box TCP bandwidth: 26-28 Gbit/s. We can ''get iperf3'' TCP bw to 45.4 Gbit/s:

# Set CPU scaling gov to 'performance' (default is 'powersave') (from 39.3 to 45.4 Gbit/s (TCP, 2 streams)) \$ for i in /sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor; do echo performance | sudo tee \$i; done

[amesfoort@drg23 ~]\$ sudo iperf3 -A 10 -B drg23-ib -s
[amesfoort@drg21 ~]\$ sudo iperf3 -N -4 -A 10 -B drg21-ib -i 1 -l 1M -P 2 -t
20 -c drg23-ib

-A 10: sets CPU affinity to hw thread 10. mlx\_4 is on the 2nd CPU (8-15(,24-31)) and the INT handler happens to be on hw thr 9
-A 10 (or another suitable nr): from 26.2 to 45.4 Gbit/s (tcp, 2 streams)

-P 2: 2 parallel streams instead of 1: from 36.4 to 45.4 Gbit/s (tcp, -A 10)

I have extensively tried the sysctl knobs of the Linux kernel networking settings (net.core.\*, net.ipv4.\*, txqueuelen), but I did not get an improvement. Likely, Linux kernel autotuning + CentOS 7 settings are already ok for local area networking up to at least 45 Gbit/s.

A real application (not a synthetic benchmark) likely does something else except for data transfer and may have trouble reaching these numbers, because CPU load is a limiting factor and the clock frequency boost of 1 core on drgXX node CPUs is higher (up to 3.2 GHz) than for all cores at once (up to 2.6 GHz). Standard drgXX node CPU clock frequency is 2.4 GHz.

Numbers (before tuning iperf3 tests) obtained using qperf can be seen below under the RDMA sub-section.

#### RDMA

RDMA (Remote Direct Memory Access) allows an application to directly access memory on another node. Although some initial administration is set up via the OS kernel, the actual transfer commands and completion handling does not go via the kernel. This also saves data copying on sender and receiver and CPU usage.

Typical applications that may use RDMA are applications that use MPI (Message Passing Interface) (such as COBALT), or (hopefully) the LUSTRE client. NFS can also be set up to use RDMA. You can program directly into the verbs and rdma-cm C APIs and link to those libraries, but be aware that extending some code to do this is not a 1 hr task... (Undoubtedly, there is also a Python module that either only wraps or even makes makes life easier.)

We used the qperf benchmark and got the following bandwidth and latency numbers between two drgXX nodes (TCP/UDP/SCTP over IP also included, but not as fast as mentioned above):

```
[amesfoort@drg22 ~]$ gperf drg23-ib sctp bw sctp lat tcp bw tcp lat udp bw
udp lat
sctp_bw:
    bw = 355 MB/sec
sctp lat:
    latency = 8.94 us
tcp bw:
    bw = 3.65 \text{ GB/sec}
tcp lat:
    latency = 6.33 us
udp bw:
    send bw = 6.12 \text{ GB/sec}
    recv bw = 3.71 \text{ GB/sec}
udp lat:
    latency = 6.26 us
[amesfoort@drg22 ~]$ sudo qperf drg23-ib rc_bi_bw rc_bw rc_lat uc_bi_bw
uc bw uc lat ud bi bw ud bw ud lat
rc bi bw:
    bw = 11.9 \text{ GB/sec}
rc bw:
    bw = 6.38 \text{ GB/sec}
rc lat:
    latency = 5.73 us
uc bi bw:
    send bw =
                   12 GB/sec
    recv bw = 11.9 \text{ GB/sec}
uc bw:
    send bw = 6.24 \text{ GB/sec}
    recv bw = 6.21 \text{ GB/sec}
uc lat:
    latency = 4.03 us
ud bi bw:
    send bw = 10.1 \text{ GB/sec}
```

```
recv bw = 10.1 \text{ GB/sec}
ud bw:
    send bw = 5.93 \text{ GB/sec}
    recv bw = 5.93 GB/sec
ud lat:
    latency = 3.94 us
[amesfoort@drg22 ~]$ sudo qperf drg23-ib rc rdma read bw rc rdma read lat
rc_rdma_write_bw rc_rdma_write_lat rc_rdma_write_poll_lat uc_rdma_write_bw
uc rdma write lat uc rdma write poll lat
rc rdma read bw:
    bw = 5.6 GB/sec
rc rdma read lat:
    latency = 4.99 us
rc rdma write bw:
    bw = 6.38 \text{ GB/sec}
rc rdma write lat:
    latency = 5.35 us
rc rdma write poll lat:
    latency = 925 ns
uc rdma write bw:
    send bw = 6.26 \text{ GB/sec}
    recv bw = 6.23 GB/sec
uc rdma write lat:
    latency = 3.58 us
uc rdma write poll lat:
    latency = 922 ns
[amesfoort@drg22 ~]$ sudo qperf drg23-ib rc_compare_swap_mr rc_fetch_add_mr
ver rc compare swap ver rc fetch add
rc compare swap mr:
    msg rate = 2.08 M/sec
rc fetch add mr:
    msg rate = 2.14 \text{ M/sec}
ver rc compare swap:
    msg_rate = 2.1 M/sec
ver rc fetch add:
    msg rate = 2.4 \text{ M/sec}
```

## **10 Gbit/s Ethernet**

The drgXX and dragproc nodes have a 10 Gbit/s ethernet adapter local to the first CPU (i.e. CPU id 0). A 48 port Ethernet switch is connected to a LOFAR core switch with 6 aggregated lines (60 Gbit/s). See below what can be achieved under ideal circumstances.

One should be able to achieve (near) 10 Gbit/s using a TCP socket stream. The iperf benchmark achieves 9.91 Gbit/s.

The 6x 10G trunk is apparently not able to utilize all 6 links simultaneously with up to 10 streams

•	is available, but off-topic here). The following is what can be max expected for DRAGNET and COBALT across 10G:
DRAGNET -> COBAL 2015)	T, 8x iperf TCP: 38.8 Gbit/s. Second run: 39.7 Gbit/s. (Aug.
	achieved 10G higher throughput using 10 COBALT and 10 DRAGNET nodes. N -B 10.168.130.1 -s and iperf -N -B 10.168.96.1 -c 1
clusters and net #streams 1 2 3 4 5 6 7 8 6 runs, half of 9 10	T (1 iperf TCP stream per node) (6x10G) (otherwise idle work) (Sep 2015) bandwidth (Gbit/s) 9.91 (1 full bw) 19.82 (2 full bw) 29.73 (3 full bw) 29.76 (2 full bw, 2 ~half bw) 29.68 (1 full bw, 4 ~half bw) 39.54 (2 full bw, 4 ~half bw) 49.49 (3 full bw, 4 ~half bw) (best result, 47-49.5) 46.19 (1 full bw, 7(?) ~half bw) (best result after the runs didn't do 8 streams) 49.28 (1 full bw, 8 ~half bw) 49.13;50.52 (0 full bw, 10 ~half bw) T (1 iperf TCP stream per node) (6x10G) (otherwise idle
clusters and net	work) (Sep 2015)
#streams 1	<pre>bandwidth (Gbit/s) 9.91 (1 full bw)</pre>
	19.82 (2 full bw)
3	19.82;9.92 (2 full bw, 2 ~half; or 3x 1/3rd)
	29.72 (2 full bw, 2 ~half)
	39.64 (3 full bw, 2 ~half) (better than dragnet->cobalt
<pre>#streams=5)</pre>	
8	49.52 (4 full bw, 3 2-3G, 1 near <1G)
10	48.87 (3 full bw, the rest 2-3 G)
From 16 CEP2 nodes	equally spread over the 4 CEP2 switches to 16 DRAGNET nodes (Sen 2015) inerf

From 16 CEP2 nodes equally spread over the 4 CEP2 switches to 16 DRAGNET nodes (Sep 2015), iperf got us in 3 test runs:

Total iperf (synthetic/benchmark) bandwidth for each test after initial ramp-up (~8 s): 48.02 Gbit/s 41.97 Gbit/s 44.84 Gbit/s If you'd transfer large, equal sized files, some files in a set of 16 would be transferred way earlier than others, since some indiv streams reached only 1 Gbit/s, while others reached 5 Gbit/s.

Doing this with 14 LOTAAS .raw files data using scp from CEP2  $\rightarrow$  DRAGNET is not going to be blazingly fast:

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```
time \
scp locus001:/data/L370522/L370522_SAP000_B000_S0_P000_bf.raw
drg01-10g:/data1/ & \
scp locus004:/data/L370522/L370522 SAP000 B002 S0 P000 bf.raw
drg02-10g:/data1/ & \
scp locus026:/data/L370522/L370522 SAP000 B021 S0 P000 bf.raw
drg03-10g:/data1/ & \
scp locus027:/data/L370522/L370522 SAP000 B022 S0 P000 bf.raw
drg04-10g:/data1/ & \
scp locus028:/data/L370522/L370522 SAP000 B023 S0 P000 bf.raw
drg05-10g:/data1/ & \
scp locus029:/data/L370522/L370522_SAP000_B024_S0_P000_bf.raw
drg06-10g:/data1/ & \
scp locus051:/data/L370522/L370522_SAP000_B045_S0_P000_bf.raw
drg07-10g:/data1/ & \
scp locus052:/data/L370522/L370522 SAP000 B046 S0 P000 bf.raw
drg08-10g:/data1/ & \
scp locus053:/data/L370522/L370522 SAP000 B047 S0 P000 bf.raw
drg09-10g:/data1/ & \
scp locus054:/data/L370522/L370522_SAP000_B048_S0_P000_bf.raw
drg10-10g:/data1/ & \
scp locus076:/data/L370522/L370522_SAP000_B068_S0_P000_bf.raw
drg11-10g:/data1/ & \
scp locus077:/data/L370522/L370522 SAP000 B069 S0 P000 bf.raw
drg12-10g:/data1/ & \
scp locus078:/data/L370522/L370522 SAP000 B070 S0 P000 bf.raw
drg13-10g:/data1/ & \
scp locus079:/data/L370522/L370522_SAP000_B071_S0_P000_bf.raw
drg14-10g:/data1/
real
        4m34.894s
                   (274.894 s)
        0m0.000s
user
        0m0.000s
sys
Total size: 14x 18982895616 bytes = 247.5087890625 Gbyte
=> 7.2 Gbit/s (14 scp streams (idle sys), no dynamic load-balancing)
```

## Storage

Only rough write tests have been done with a sequential dd(1). Disk I/O bandwidth changes across the platters. Actual file I/O also depends on how the filesystem lays out the data.

## drgXX nodes

Scratch space on drgXX at /data1 and /data2 (individually). A transfer size of 4k vs 64k does not appear to matter. We reach up to 288 MiB/s.

## dragproc node

On dragproc at /data, a transfer size of 64k may perform somewhat better than 4k, but not consistently. We reach 490 - 530 MiB/s.

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